## REVISED EXAMINATION TIME TABLE

**Time : 2.00 PM to 5.00 PM**

<table>
<thead>
<tr>
<th>Date and Day</th>
<th>Structural Engineering</th>
<th>Geo-Technical Engineering</th>
<th>Water Resources Engineering</th>
<th>Infrastructure Engineering</th>
<th>Transportation Engg.</th>
<th>Construction Engineering &amp; Management</th>
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<tbody>
<tr>
<td>18-07-2016</td>
<td>Pre stressed Concrete</td>
<td>Pre-stressed Concrete</td>
<td>Expansive Soil Engineering</td>
<td>Advanced Fluid Mechanics</td>
<td>Pre stressed Concrete</td>
<td>Analysis of Transportation Systems</td>
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<tr>
<td>MONDAY</td>
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<td>Design of Highway Infrastructure</td>
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<tr>
<td>FRIDAY</td>
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<tr>
<td>MONDAY</td>
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<tr>
<td>27-07-2016</td>
<td>Advanced Steel Design</td>
<td></td>
<td>Foundation Engg. - II</td>
<td>Infrastructure Development</td>
<td>Transportation Modelling and Simulation</td>
<td>Urban Transportation Systems Planning</td>
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<tr>
<td>WEDNESDAY</td>
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<tr>
<td>02-08-2016</td>
<td>Advanced Concrete Technology</td>
<td>Engineering Research Methodology</td>
<td>Advanced Concrete Technology</td>
<td>Project Performance Management</td>
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<tr>
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**Note:**
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**DIRECTOR OF EVALUATION EXAMINATION CELL**
### UNIVERSITY COLLEGE OF ENGINEERING
OSMANIA UNIVERSITY
M.E (Elect. Engg.) II - Semester (Regular & PTPG) Main Examination
July/August 2016

**REVISED EXAMINATION TIME TABLE**

*Time: 2.00 PM to 5.00 PM*

<table>
<thead>
<tr>
<th>Date &amp; Day</th>
<th>Industrial Drives and Control</th>
<th>Power Systems</th>
<th>Power Electronic Systems</th>
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<tbody>
<tr>
<td></td>
<td>Regular II - Semester</td>
<td>PTPG II - Semester</td>
<td>Regular II - Semester</td>
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<tr>
<td>20-07-2016 WEDNESDAY</td>
<td>Machine Modelling and Analysis</td>
<td>Machine Modelling and Analysis</td>
<td>Power Quality Engineering</td>
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<tr>
<td>22-07-2016 FRIDAY</td>
<td>Static Control of AC Drives</td>
<td>= = = = = = = = = =</td>
<td>Advanced Power System Protection</td>
</tr>
<tr>
<td>25-07-2016 MONDAY</td>
<td>High Voltage DC Transmission</td>
<td>Digital Circuits and Logic Design</td>
<td>Power System Deregulation</td>
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<tr>
<td>27-07-2016 WEDNESDAY</td>
<td>Special Electrical Machines</td>
<td>= = = = = = = = = =</td>
<td>Power System Stability</td>
</tr>
<tr>
<td>29-07-2016 FRIDAY</td>
<td>Control of Electric Drives</td>
<td>= = = = = = = = = =</td>
<td>Electric and Hybrid Electrical Vehicles</td>
</tr>
<tr>
<td>02-08-2016 TUESDAY</td>
<td>Dynamics of Electric Drives</td>
<td>Engineering Research Methodology</td>
<td>Real Time Applications in Power Systems</td>
</tr>
</tbody>
</table>

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DIRECTOR OF EVALUATION
EXAMINATION CELL
<table>
<thead>
<tr>
<th>Date and Day</th>
<th>Digital Systems</th>
<th>Systems &amp; Signal Processing</th>
<th>Microwave &amp; Radar Engineering</th>
<th>Embedded Systems and VLSI Design</th>
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</thead>
<tbody>
<tr>
<td>18-07-2016</td>
<td>ASIC Verification with System Verilog</td>
<td>Digital Spectral Analysis</td>
<td>Radar Systems Engineering</td>
<td>VLSI Physical Design</td>
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<td>ASIC Verification with System Verilog</td>
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<tr>
<td>20-07-2016</td>
<td>Advanced Computer Communication Networks</td>
<td>Image &amp; Video Processing</td>
<td>Microwave Integrated Circuits</td>
<td>Scripting Languages for VLSI Design Automation</td>
</tr>
<tr>
<td>WEDNESDAY</td>
<td>Advanced Computer Communication Networks</td>
<td></td>
<td></td>
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</tr>
<tr>
<td>FRIDAY</td>
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<tr>
<td>25-07-2016</td>
<td></td>
<td></td>
<td>Microwave Antennas</td>
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</tr>
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<td>MONDAY</td>
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<tr>
<td>WEDNESDAY</td>
<td>VLSI Design &amp; Technology</td>
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<td>FRIDAY</td>
<td>Advanced Computer Networks</td>
<td></td>
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<td>Optimization Techniques</td>
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<td>02-08-2016</td>
<td>Coding Theory &amp; Techniques</td>
<td>Coding Theory &amp; Techniques</td>
<td>Satellite Radio Navigation</td>
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<td>VLSI Technology</td>
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<td>04-08-2016</td>
<td>System on Chip Architecture</td>
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<td>Engineering Mathematics</td>
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<td>THURSDAY</td>
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<td>Electromagnetic Field Theory</td>
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</table>

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**DIRECTOR OF EVALUATION EXAMINATION CELL**
**UNIVERSITY COLLEGE OF ENGINEERING**  
**OSMANIA UNIVERSITY, HYDERABAD - 7**  
**M. E. (BME) II - Semester (Main) Examination**  
**July/August 2016**  
**EXAMINATION TIME TABLE**  
**Time : 2.00 PM to 5.00 PM**

<table>
<thead>
<tr>
<th>Date &amp; Day</th>
<th>BIOMEDICAL ELECTRONICS II - SEMESTER</th>
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<tbody>
<tr>
<td>18-07-2016</td>
<td>ADVANCED MEDICAL IMAGING</td>
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<tr>
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<tr>
<td>20-07-2016</td>
<td>DIAGNOSTIC AND THERAPEUTIC EQUIPMENT</td>
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<td>ADVANCED BIOMATERIALS</td>
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<td>25-07-2016</td>
<td>MEDICAL IMAGE PROCESSING</td>
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<tr>
<td>MONDAY</td>
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<tr>
<td>27-07-2016</td>
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</tr>
<tr>
<td>WEDNESDAY</td>
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<tr>
<td>29-07-2016</td>
<td>ADVANCED BIO-MEDICAL SIGNAL PROCESSING</td>
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<tr>
<td>02-08-2016</td>
<td>BIO NANO TECHNOLOGY</td>
</tr>
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</table>

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DIRECTOR OF EVALUATION  
EXAMINATION CELL
<table>
<thead>
<tr>
<th>Date &amp; Day</th>
<th>Production Engineering</th>
<th>Automation &amp; Robotics</th>
<th>Turbo Machinery</th>
<th>CAD/CAM</th>
<th>Tool Design</th>
<th>Design for Manufacturing</th>
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<tbody>
<tr>
<td>18-07-2016</td>
<td>Advanced Manufacturing</td>
<td>Vibration Analysis &amp;</td>
<td>Design of Gas</td>
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<td>Condition Monitoring</td>
<td>Turbine</td>
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<td>20-07-2016</td>
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<td>Computer Aided</td>
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<td>Automation</td>
<td>Design of Manufacturing</td>
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<tr>
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<td>Management</td>
<td>Mechanical Design &amp;</td>
<td>Mechanical Design &amp;</td>
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<td>Advanced Casting and</td>
<td>Advanced Manufacturing</td>
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<td>Advanced Kinematics</td>
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<td>Design of Dies</td>
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<td>Material Science and</td>
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<td>Fluid Dynamics</td>
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<td>02-08-2016</td>
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<td>Metal Cutting &amp;</td>
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<td>04-08-2016</td>
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</tbody>
</table>

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UNIVERSITY COLLEGE OF ENGINEERING
OSMANIA UNIVERSITY, HYDERABAD
M. Tech. (C.S.E.) II - Semester (Regular & PTPG) Main Examination
July/August 2016

REVISED EXAMINATION TIME TABLE

Time: 2.00 PM to 5.00 PM

<table>
<thead>
<tr>
<th>DATE &amp; DAY</th>
<th>Parallel &amp; Distributed Systems</th>
<th>Embedded Systems &amp; Computing</th>
<th>CSE</th>
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<td>REGULAR II - SEMESTER</td>
<td>REGULAR II - SEMESTER</td>
<td>REGULAR II - SEMESTER</td>
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<tr>
<td>MONDAY</td>
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<td>Hardware and Software</td>
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<td>WEDNESDAY</td>
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<td>Co-design</td>
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<td>Embedded Programming</td>
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<td>Advanced Algorithms</td>
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<td>Parallel Programming</td>
<td>System on Chip Architecture</td>
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<tr>
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<td>Software Project Management</td>
<td>Low Power VLSI and Design</td>
<td>Image Processing</td>
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<tr>
<td>02-08-2016</td>
<td>Web Mining</td>
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<td>Methodology</td>
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<tr>
<td>04-08-2016</td>
<td>Network Security</td>
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<td>THURSDAY</td>
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**UNIVERSITY COLLEGE OF ENGINEERING**  
**OSMANIA UNIVERSITY**  
M. E. / M.Tech. (Civil & IT) II - Semester (Main) Examination  
July/August 2016

**EXAMINATION TIME TABLE**  
Time: 2.00 PM to 5.00 PM

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<thead>
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<th>I.T.</th>
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<td>SE (Earthquake Engineering)</td>
<td>(Computer Networks &amp; Information Security)</td>
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<td>Cloud Computing</td>
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<td>20-07-2016</td>
<td>Repairs and Retro Fitting of Structures</td>
<td>Advanced Earthquake Resistant Design of Structures</td>
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<tr>
<td>WEDNESDAY</td>
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<tr>
<td>22-07-2016</td>
<td>Structural Design</td>
<td>Earthquake Resistant Design of Structures</td>
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<tr>
<td>FRIDAY</td>
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<tr>
<td>25-07-2016</td>
<td>Theory of Plates</td>
<td>Seismic Evaluation &amp; Retrofiting of Structures</td>
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<tr>
<td>MONDAY</td>
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<tr>
<td>27-07-2016</td>
<td>Advanced Steel Design</td>
<td>Soil Dynamics &amp; Machine Foundations</td>
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<td>29-07-2016</td>
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<td>Finite Element Methods</td>
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<tr>
<td>02-08-2016</td>
<td>Theory of Shell &amp; Folded Plates</td>
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</tr>
<tr>
<td>TUESDAY</td>
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**Note:** This Time-Table is also applicable for Pre-Ph.D. students.
<table>
<thead>
<tr>
<th>Date &amp; Day</th>
<th>CIVIL ENGINEERING</th>
<th>ELECTRONICS AND COMMUNICATION ENGINEERING (Digital Systems)</th>
<th>C. S. E.</th>
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<tbody>
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<tr>
<td>20-07-2016</td>
<td>Theory of Elasticity</td>
<td>GNSS Signals and Receiver Technology</td>
<td>Advanced Databases</td>
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<tr>
<td>WEDNESDAY</td>
<td>Road Safety and Traffic Management</td>
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<td>22-07-2016</td>
<td>Neural, Fuzzy &amp; Expert Systems</td>
<td>Optical Fibre Communication Systems</td>
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<td>FRIDAY</td>
<td>Rural Roads</td>
<td>Wireless Mobile Communication Systems</td>
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<td>25-07-2016</td>
<td>Theory of Plates</td>
<td>Pavement Systems Engineering</td>
<td>Distributed Computing</td>
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<td>MONDAY</td>
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<tr>
<td>27-07-2016</td>
<td>Transportation Modelling and Simulation</td>
<td>Digital System Design</td>
<td>Human Computer Interaction</td>
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<td>WEDNESDAY</td>
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<td></td>
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<tr>
<td>29-07-2016</td>
<td>Finite Element Methods</td>
<td>Advanced Computer Networks</td>
<td>Software Project Management</td>
</tr>
<tr>
<td>FRIDAY</td>
<td>Rural &amp; Regional Transportation Systems</td>
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<tr>
<td>02-08-2016</td>
<td>Advanced Concrete Technology</td>
<td>Analog &amp; Mixed Signal IC Design</td>
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<tr>
<td>TUESDAY</td>
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<tr>
<td>04-08-2016</td>
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<td>Network Security</td>
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<tr>
<td>THURSDAY</td>
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DIRECTOR OF EVALUATION
EXAMINATION CELL
<table>
<thead>
<tr>
<th>DATE &amp; DAY</th>
<th>CIVIL (Structural Engg.)</th>
<th>E. E. (Power Electronics Systems)</th>
<th>E. C. E. (Digital Systems)</th>
<th>MECHANICAL (CAD / CAM)</th>
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<td>Machine Modeling and Analysis</td>
<td>Image and Video Processing</td>
<td>Automation</td>
<td>Advanced Databases</td>
</tr>
<tr>
<td>22-07-2016 FRIDAY</td>
<td>Neural Fuzzy &amp; Expert Systems</td>
<td>Advanced Topics in Power Electronics</td>
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</tr>
<tr>
<td>27-07-2016 WEDNESDAY</td>
<td>Advanced Steel Design</td>
<td>Industrial Electronics Systems</td>
<td>——</td>
<td>Failure Analysis and Design</td>
<td>Human Computer Interaction</td>
</tr>
<tr>
<td>29-07-2016 FRIDAY</td>
<td>Structural Dynamics</td>
<td>——</td>
<td>Advanced Computer Networks</td>
<td>——</td>
<td>Software Project Management</td>
</tr>
<tr>
<td>02-08-2016 TUESDAY</td>
<td>Advanced Reinforced Concrete Design</td>
<td>Engineering Research Methodology</td>
<td>Wireless Channel Coding Techniques</td>
<td>Engineering Research Methodology</td>
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</tr>
<tr>
<td>04-08-2016 THURSDAY</td>
<td>——</td>
<td>System on Chip Design</td>
<td>Computer Integrated Manufacturing</td>
<td>Network Security</td>
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</tbody>
</table>

Note: This Time-Table is also applicable for Pre-Ph.D. students registered for December 2013/January 2014 Examination.

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# UNIVERSITY COLLEGE OF ENGINEERING
OSMANIA UNIVERSITY, HYDERABAD
M. E. (ECE) II - Semesters (Main) Examination
July/August 2016

STANLEY COLLEGE OF ENGINEERING & TECHNOLOGY FOR WOMEN
EXAMINATION TIME TABLE

**TIME: 2.00 PM TO 5.00 PM**

<table>
<thead>
<tr>
<th>DATE &amp; DAY</th>
<th>E. C. E. (EMBEDDED SYSTEMS)</th>
<th>II-Semester</th>
</tr>
</thead>
<tbody>
<tr>
<td>18-07-2016 MONDAY</td>
<td>Advanced Digital Design with Verilog HDL</td>
<td></td>
</tr>
<tr>
<td>20-07-2016 WEDNESDAY</td>
<td>Image and Video Processing</td>
<td></td>
</tr>
<tr>
<td>22-07-2016 FRIDAY</td>
<td>== == ==</td>
<td></td>
</tr>
<tr>
<td>25-07-2016 MONDAY</td>
<td>Real Time Operating System</td>
<td></td>
</tr>
<tr>
<td>27-07-2016 WEDNESDAY</td>
<td>Digital Signal Processors</td>
<td></td>
</tr>
<tr>
<td>29-07-2016 FRIDAY</td>
<td>Advanced Computer Networks</td>
<td></td>
</tr>
<tr>
<td>02-08-2016 TUESDAY</td>
<td>== == ==</td>
<td></td>
</tr>
<tr>
<td>04-08-2016 THURSDAY</td>
<td>System on Chip Design</td>
<td></td>
</tr>
</tbody>
</table>

**Note:** This Time-Table is also applicable for Pre-Ph.D. students.

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DIRECTOR OF EVALUATION
EXAMINATION CELL
**UNIVERSITY COLLEGE OF ENGINEERING**  
OSMANIA UNIVERSITY, HYDERABAD  
M. E. (ECE, Mechanical & CSE) II - Semesters (Main) Examination  
July/August 2016  
**METHODIST COLLEGE OF ENGINEERING & TECHNOLOGY**  
**REVISED EXAMINATION TIME TABLE**

**TIME : 2.00 PM TO 5.00 PM**

<table>
<thead>
<tr>
<th>DATE &amp; DAY</th>
<th>MECHANICAL (CAD/CAM) II-Semester</th>
</tr>
</thead>
<tbody>
<tr>
<td>18-07-2016 MONDAY</td>
<td>Flexible Manufacturing Systems / Vibration Analysis and Condition Monitoring</td>
</tr>
<tr>
<td>20-07-2016 WEDNESDAY</td>
<td>Computer Aided Mechanical Design &amp; Analysis</td>
</tr>
<tr>
<td>22-07-2016 FRIDAY</td>
<td>Design of Press Tools</td>
</tr>
<tr>
<td>25-07-2016 MONDAY</td>
<td>Control of Dynamic Systems</td>
</tr>
<tr>
<td>27-07-2016 WEDNESDAY</td>
<td>Failure Analysis and Design</td>
</tr>
<tr>
<td>29-07-2016 FRIDAY</td>
<td>Computer Aided Modeling &amp; Design</td>
</tr>
<tr>
<td>02-08-2016 TUESDAY</td>
<td>Engineering Research Methodology</td>
</tr>
<tr>
<td>04-08-2016 THURSDAY</td>
<td>= = = = = =</td>
</tr>
</tbody>
</table>

*Note: This Time-Table is also applicable for Pre-Ph.D. students.*

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DIRECTOR OF EVALUATION EXAMINATION CELL
UNIVERSITY COLLEGE OF ENGINEERING  
OSMANIA UNIVERSITY, HYDERABAD - 7  
M. E. (ECE, Mechanical & CSE) II - Semesters (Main) Examination  
July/August 2016  

M V S R ENGINEERING COLLEGE  
REVISED EXAMINATION TIME-TABLE  

Time: 2.00 PM to 5.00 PM

<table>
<thead>
<tr>
<th>DATE &amp; DAY</th>
<th>E. C. E. (EMBEDDED SYSTEMS &amp; VLSI DESIGN)</th>
<th>MECHANICAL (CAD / CAM)</th>
<th>C. S. E.</th>
</tr>
</thead>
<tbody>
<tr>
<td>18-07-2016 MONDAY</td>
<td>VLSI Physical Design</td>
<td>Flexible Manufacturing Systems</td>
<td>Cloud Computing</td>
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<tr>
<td>20-07-2016 WEDNESDAY</td>
<td>Scripting Languages for VLSI Design Automation</td>
<td>Automation</td>
<td>Advanced Databases</td>
</tr>
<tr>
<td>22-07-2016 FRIDAY</td>
<td>Low Power VLSI Design</td>
<td>Additive Manufacturing Technologies &amp; Applications</td>
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<tr>
<td>25-07-2016 MONDAY</td>
<td>Real Time Operating Systems</td>
<td>Control of Dynamic Systems</td>
<td>Distributed Computing</td>
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<tr>
<td>27-07-2016 WEDNESDAY</td>
<td>= = = = = = = = = = = = = = = = = = = =</td>
<td>= = = = = = = = = = = = = = = = = = = =</td>
<td>Machine Learning</td>
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<tr>
<td>29-07-2016 FRIDAY</td>
<td>Digital IC Design</td>
<td>Computer Aided Modelling &amp; Design</td>
<td>Image Processing</td>
</tr>
<tr>
<td>02-08-2016 TUESDAY</td>
<td>Field Programmable Gate Arrays</td>
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<tr>
<td>04-08-2016 THURSDAY</td>
<td>Advanced Computer Organization</td>
<td>Finite Element Techniques</td>
<td>Network Security</td>
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</tbody>
</table>

**Note:** This Time-Table is also applicable for Pre-Ph.D. students.

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EXAMINATION CELL
**UNIVERSITY COLLEGE OF ENGINEERING**  
**OSMANIA UNIVERSITY, HYDERABAD**  
**M. E. (ECE & CSE) II - Semesters (Main) Examination**  
**July/August 2016**  
**Swathi Institute of Technology & Science**  
**REVISED EXAMINATION TIME TABLE**  
**TIME : 2.00 PM TO 5.00 PM**

<table>
<thead>
<tr>
<th>DATE &amp; DAY</th>
<th>E. C. E. (EMBEDDED SYSTEMS) II-Semester</th>
<th>C. S. E. (SOFTWARE ENGINEERING) II-Semester</th>
<th>C. S. E. II-Semester</th>
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<tbody>
<tr>
<td>18-07-2016 MONDAY</td>
<td>CPLD &amp; FPGA Architectures &amp; Applications</td>
<td>Cloud Computing</td>
<td>Cloud Computing</td>
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<tr>
<td>20-07-2016 WEDNESDAY</td>
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<td>Secure Software Engineering</td>
<td>Advanced Databases</td>
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<tr>
<td>22-07-2016 FRIDAY</td>
<td>= = = = = = = = = = = = =</td>
<td>Mobile Computing</td>
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<tr>
<td>25-07-2016 MONDAY</td>
<td>Wireless Mobile Communication Systems</td>
<td>Distributed Component Architecture</td>
<td>Distributed Computing</td>
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<td>27-07-2016 WEDNESDAY</td>
<td>DSP Processors Architecture</td>
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<td>Human Computer Interaction</td>
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<tr>
<td>29-07-2016 FRIDAY</td>
<td>Digital IC Design</td>
<td>Software Process Management</td>
<td>Software Project Management</td>
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<tr>
<td>02-08-2016 TUESDAY</td>
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<td>Semantic Web</td>
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<tr>
<td>04-08-2016 THURSDAY</td>
<td>System on Chip Architecture</td>
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<td>Network Security</td>
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<tr>
<td>06-08-2016 SATURDAY</td>
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<td>= = = = = = = = = = = = =</td>
<td>Information Retrieval Systems</td>
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</tbody>
</table>

**Note:** This Time-Table is also applicable for Pre-Ph.D. students.